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SERIAL NUMBER 10/663,371	FILING DATE 09/16/2003 RULE	CLASS 257	GROUP ART UNIT '2815	ATTORNEY DOCKET NO. Via Interconnect Patent A
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** CONTINUING DATA *****

This appln claims benefit of 60/410,895 09/16/2002 *nbe*** FOREIGN APPLICATIONS ***** *nbe*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY **

** 12/05/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 5	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>James Boyd</i> Examiner's Signature <i>nbe</i> Initials				

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TITLE

Stacked microelectronic module with vertical interconnect vias

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of
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